

# Fast quantum integer multiplication with almost no ancillas

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Gregory D. Kahanamoku-Meyer

July 14, 2023

Arithmetic on quantum computers: why do we care?

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OPEN

### Classically verifiable quantum advantage from a computational Bell test


Gregory D. Kahanamoku-Meyer<sup>1</sup>, Soonwon Choi<sup>1</sup>, Umesh V. Vazirani<sup>2</sup> and Norman Y. Yao<sup>1</sup>

Existing experimental demonstrations of quantum computational advantage have had the limitation that verifying the correctness of the quantum device requires exponentially costly classical computations. Here we propose and analyse an interactive protocol for demonstrating quantum computational advantage, which is efficiently classically verifiable. Our protocol relies on a class of cryptographic tools called trapdoor claw-free functions. Although this type of function has been applied to quantum advantage protocols before, our protocol employs a surprising connection to Bell's inequality to avoid the need for a demanding cryptographic property called the adaptive hardcore bit, while maintaining essentially no increase in the quantum circuit complexity and no extra assumptions. Leveraging the relaxed cryptographic requirements of the protocol, we present two trapdoor claw-free function constructions, based on Rabin's function and the Diffie-Hellman problem, which have not been used in this context before. We also

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### Classically verifiable quantum advantage from a computational E

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Existing experimental demonstrations of quantum advantage require exponentially more quantum resources than classical algorithms for demonstrating quantum computational advantage. Our protocol employs a cryptographic tool called trapdoor claw-free functions, which are a type of cryptographic primitive that is believed to be secure under relaxed cryptographic assumptions. Leveraging the relaxed cryptographic assumptions, based on Rabin's function and the existence of a one-way function, we propose a protocol for demonstrating quantum advantage that is classically verifiable.

### A Cryptographic Test of Quantumness and Certifiable Randomness from a Single Quantum Device

ZVIKA BRAKERSKI, Weizmann Institute of Science, Israel  
PAUL CHRISTIANO, OpenAI, USA  
URMILA MAHADEV, California Institute of Technology, USA  
UMESH VAZIRANI, UC Berkeley, USA  
THOMAS VIDICK, California Institute of Technology, USA

We consider a new model for the testing of untrusted quantum devices, consisting of a single polynomial time bounded quantum device interacting with a classical polynomial time verifier. In this model, we propose solutions to two tasks—a protocol for efficient classical verification that the untrusted device is “truly quantum” and a protocol for producing certifiable randomness from a single untrusted quantum device. Our solution relies on the existence of a new cryptographic primitive for constraining the power

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SIAM J. COMPUT.  
Vol. 26, No. 5, pp. 1484–1509, October 1997

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009

### POLYNOMIAL-TIME ALGORITHMS FOR PRIME FACTORIZATION AND DISCRETE LOGARITHMS ON A QUANTUM COMPUTER\*

PETER W. SHOR<sup>†</sup>

**Abstract.** A digital computer is generally believed to be an efficient universal computing device; that is, it is believed able to simulate any physical computing device with an increase in computation time by at most a polynomial factor. This may not be true when quantum mechanics is taken into consideration. This paper considers factoring integers and finding discrete logarithms, two problems which are generally thought to be hard on a classical computer and which have been used as the basis for public-key cryptography. Efficient quantum algorithms are given for factoring integers and finding discrete logarithms, two problems which are generally thought to be hard on a classical computer and which have been used as the basis for public-key cryptography.

# Multiplication on quantum computers

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# Multiplication on quantum computers

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$$\mathcal{U}_{q \times q} |x\rangle |y\rangle |w\rangle = |x\rangle |y\rangle |w + xy\rangle$$

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... with as few gates and qubits as possible.

1. Fast multiplication (few gates)

1. Fast multiplication (few gates)
2. Quantum multiplication (few qubits)

# Overview

1. Fast multiplication (few gates)
2. Quantum multiplication (few qubits)
3. Fast quantum multiplication (few gates + qubits)

## Background: schoolbook multiplication

The “schoolbook” method:  $xy = \sum_{ij}(2^i x_i)(2^j y_j) = \sum_{ij} 2^{i+j} x_i y_j$

$$\begin{array}{r} \phantom{+} \phantom{1} \phantom{0} \phantom{0} \phantom{0} \phantom{0} \phantom{1} \phantom{0} \\ \phantom{+} \phantom{1} \phantom{0} \phantom{0} \phantom{0} \phantom{0} \phantom{1} \phantom{0} \\ \phantom{+} \phantom{1} \phantom{0} \phantom{0} \phantom{0} \phantom{0} \phantom{1} \phantom{0} \\ \times \phantom{1} \phantom{0} \phantom{1} \phantom{0} \\ \hline \phantom{+} \phantom{1} \phantom{0} \phantom{0} \phantom{0} \phantom{0} \phantom{1} \phantom{0} \\ \phantom{+} \phantom{1} \phantom{0} \phantom{0} \phantom{0} \phantom{0} \phantom{1} \phantom{0} \\ \phantom{+} \phantom{1} \phantom{0} \phantom{0} \phantom{0} \phantom{0} \phantom{1} \phantom{0} \\ \phantom{+} \phantom{1} \phantom{0} \phantom{0} \phantom{0} \phantom{0} \phantom{1} \phantom{0} \\ + \phantom{1} \phantom{0} \phantom{1} \phantom{0} \\ \hline \mathbf{1} \ \mathbf{0} \ \mathbf{0} \ \mathbf{0} \ \mathbf{0} \ \mathbf{0} \ \mathbf{1} \ \mathbf{0} \end{array}$$

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Running time:  $\mathcal{O}(n^2)$  operations

## Background: schoolbook multiplication

Given two  $n$ -bit numbers  $x$  and  $y$ , what if we use base  $b = 2^{n/2}$ ?

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$$xy = x_1 y_1 b^2 + x_0 y_1 b + x_1 y_0 b + x_0 y_0$$

Time remains  $\mathcal{O}(n^2)$ , because  $4(n/2)^2 = n^2$

## Background: Karatsuba multiplication

$$xy = x_1y_1b^2 + (x_0y_1 + x_1y_0)b + x_0y_0$$

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**Observation:**  $x_0y_1 + x_1y_0 = (x_1 + x_0)(y_1 + y_0) - x_1y_1 - x_0y_0$

Can compute  $xy$  with only **three** multiplications of size  $n/2$ :

1.  $x_1y_1$
2.  $x_0y_0$
3.  $(x_1 + x_0)(y_1 + y_0)$

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$$xy = x_1y_1b^2 + (x_0y_1 + x_1y_0)b + x_0y_0$$

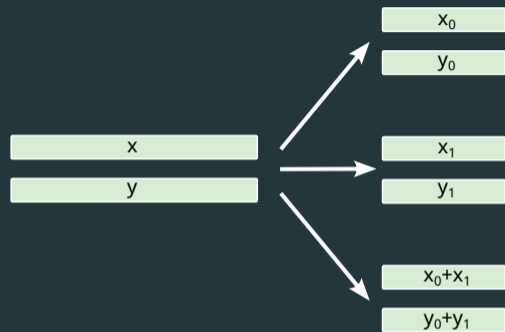
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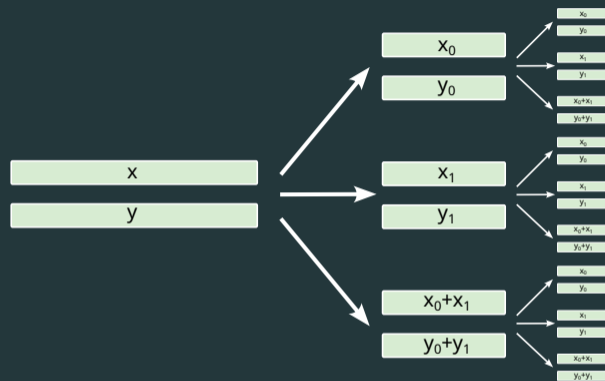
1.  $x_1y_1$
2.  $x_0y_0$
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Computational cost:  $3(n/2)^2 = \frac{3}{4}n^2 = \mathcal{O}(n^2)$

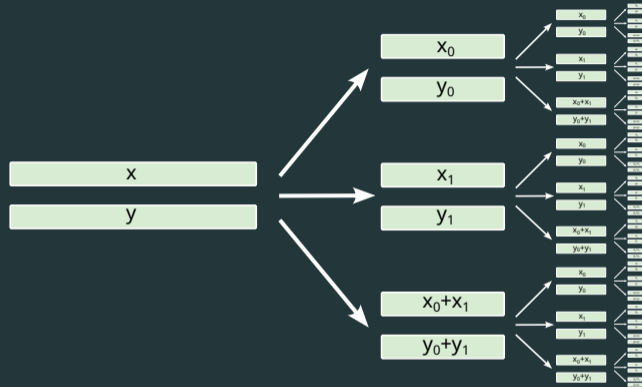
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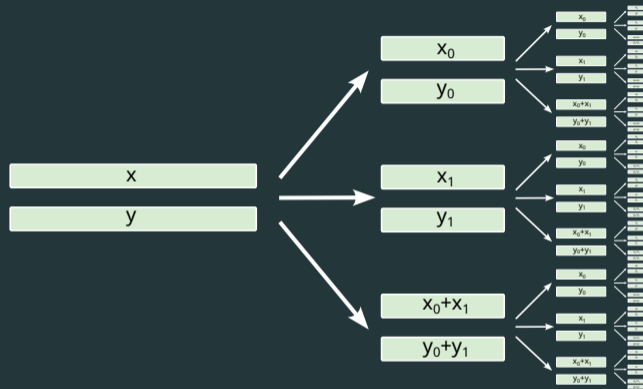


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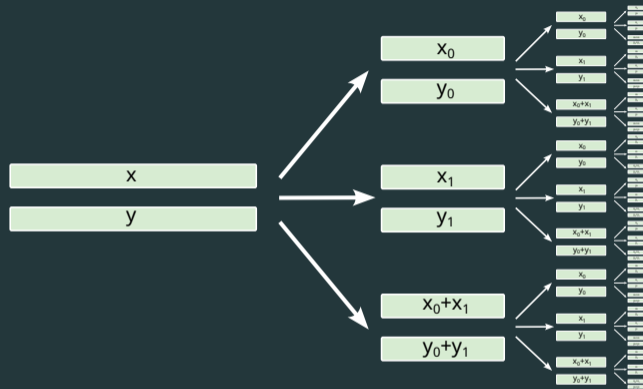


# Background: Karatsuba multiplication



Depth:  $d = \log_2 n$

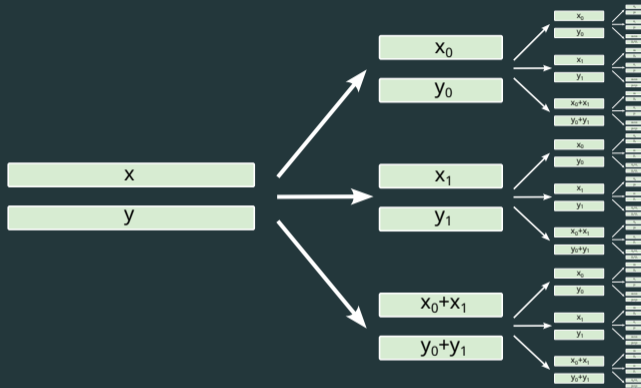
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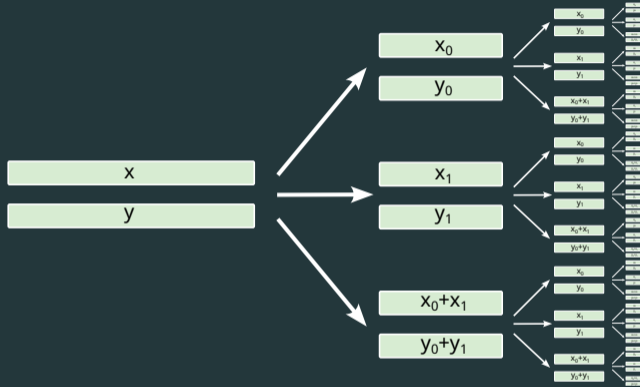


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Recursion relation:  $T(n) \sim 3T(n/2)$

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Depth:  $d = \log_2 n$

Operations:  $3^d$

Recursion relation:  $T(n) \sim 3T(n/2)$

Solution:  $T(n) = \mathcal{O}(n^{\log_2 3}) = \mathcal{O}(n^{1.58\dots})$

## Background: Karatsuba multiplication

**Question:** why don't we always do this, classically?

**Answer:** the extra complexity isn't always worth it!

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GNU multiple-precision arithmetic library cutoff: 2176 bit numbers

## Background: Toom-Cook multiplication

Break the  $n$  bit numbers into  $k$  chunks of  $n/k$  bits.



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Algorithm	Gate count
Schoolbook	$\mathcal{O}(n^2)$
$k = 2$	$\mathcal{O}(n^{\log_2 3}) = \mathcal{O}(n^{1.58\dots})$
$k = 3$	$\mathcal{O}(n^{\log_3 5}) = \mathcal{O}(n^{1.46\dots})$
$k = 4$	$\mathcal{O}(n^{\log_4 7}) = \mathcal{O}(n^{1.40\dots})$

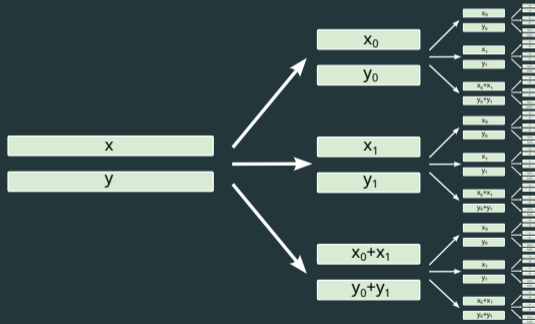
## Summary: fast multiplication

- “Standard” multiplication requires time  $\mathcal{O}(n^2)$  operations
- Faster algorithms exist, but have large constant factors

Can these fast circuits be made quantum?

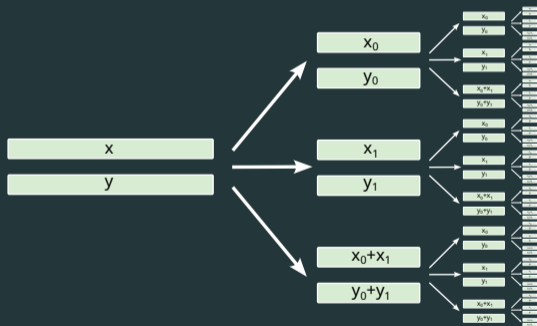
# Can these fast circuits be made quantum?

Challenge: making recursive algorithms reversible



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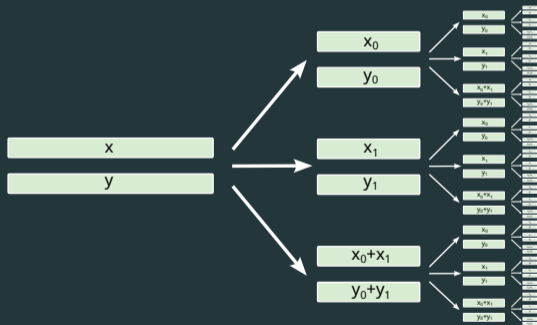
Challenge: making recursive algorithms reversible



Work	Qubits
Kowada et al. '06	$\mathcal{O}(n^{1.58\dots})$
Parent et al. '18	$\mathcal{O}(n^{1.43\dots})$
Gidney '19	$\mathcal{O}(n)$

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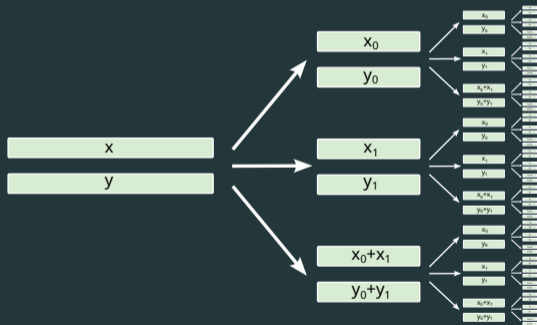


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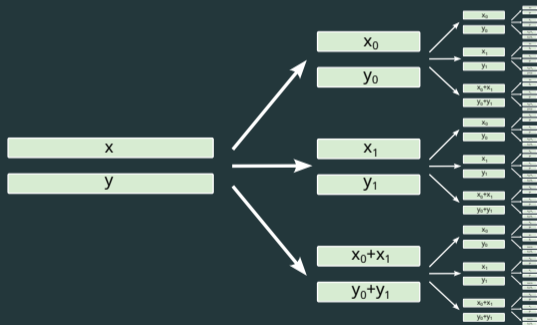
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**Result:** Fast multiplication using 1 ancilla

# A fundamentally quantum way of doing arithmetic

Quantum Fourier transform:

$$QFT |w\rangle = \sum_z \exp\left(\frac{2\pi iwz}{2^n}\right) |z\rangle$$

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$$\exp\left(\frac{2\pi ixyz}{2^n}\right) = \prod_{i,j,k} \exp\left(\frac{2\pi i 2^{i+j+k}}{2^n} x_i y_j z_k\right)$$

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A series of  $CCR_\phi$  gates between the bits of  $|x\rangle$ ,  $|y\rangle$ , and  $|z\rangle$ !

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$$\exp\left(\frac{2\pi ixyz}{2^n}\right) = \prod_{i,j,k} \exp\left(\frac{2\pi i2^{i+j+k}}{2^n} x_i y_j z_k\right)$$

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$$\exp\left(\frac{2\pi iaxz}{2^n}\right) = \prod_{i,j} \exp\left(\frac{2\pi ia2^{i+j}}{2^n} x_i z_j\right)$$

Here:  $\mathcal{O}(n^2)$  controlled phase rotations (matches Schoolbook algorithm)



**Main question:** Can we combine fast multiplication with Fourier arithmetic to get the benefits of both?

# Fast classical-quantum multiplication

Goal:  $\mathcal{U}(a) |x\rangle |0\rangle = |x\rangle |ax\rangle$

# Fast classical-quantum multiplication

Goal: Apply phase  $\exp\left(\frac{2\pi ia}{2^n}xz\right)$ ;  $x$  and  $z$  are quantum

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Goal: Implement  $\text{PhaseProduct}(\phi) |x\rangle |z\rangle = \exp(i\phi xz) |x\rangle |z\rangle$

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**Goal:** Implement  $\text{PhaseProduct}(\phi) |x\rangle |z\rangle = \exp(i\phi xz) |x\rangle |z\rangle$

We want to split the phase  $\phi xz$  into the sum of many phases, which are easy to implement.

# Fast classical-quantum multiplication

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$$xz = 2^n x_1 z_1 + 2^{n/2} ((x_0 + x_1)(z_0 + z_1) - x_0 z_0 - x_1 z_1) + x_0 z_0$$

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How are we supposed to **reuse** values in the *phase*?

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Each of these has the same structure, but on half as many qubits  $\rightarrow$  do it recursively!

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**Complexity:**  $T(n) = 3T(n/2) \Rightarrow \mathcal{O}(n^{\log_2 3}) = \mathcal{O}(n^{1.58\dots})$  gates!

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Splitting registers  $|x\rangle \rightarrow |x_1\rangle |x_0\rangle$  and  $|z\rangle \rightarrow |z_1\rangle |z_0\rangle$ , can immediately do

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Goal:  $\mathcal{U} |x\rangle |y\rangle |0\rangle = |x\rangle |y\rangle |xy\rangle$

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Goal: Apply phase  $\exp\left(\frac{2\pi i}{2^n}xyz\right)$ ;  $x$ ,  $y$ , and  $z$  are quantum

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Doesn't work in the phase!!



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$k$	Runtime
2	$\mathcal{O}(n^2)$
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These runtimes are achieved with 2 ancilla qubits.





## Depth considerations

Parallelization is natural—achieving depth  $\mathcal{O}(n)$  is easy!

But  $\mathcal{O}(n^{1.58})/n = \mathcal{O}(n^{0.58})$  is our lower bound: can we get there?

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**Claim:** Can implement PhaseProduct and PhaseTripleProduct in sub-linear time, using  $\mathcal{O}(n)$  ancillas!

Surprisingly, multiplication itself is bottlenecked by QFT

So far: have been using phase

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Goal: only use  $n$  bits for output modulo  $N$

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Output register requires  $n + \mathcal{O}(\log(1/\epsilon))$  qubits

# Summary

## Classical-quantum

1 ancilla qubit

$k$	Gates
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3	$\mathcal{O}(n^{1.46\dots})$
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**Exact QFT** in  $\mathcal{O}(n^{1.46})$  gates using 1 ancilla

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- How well can we optimize explicit circuits?

**Thank you!**

Greg Kahanamoku-Meyer — [gkm@berkeley.edu](mailto:gkm@berkeley.edu)

# Backup

# Fast classical-quantum multiplication: algorithm

PhaseProduct( $\phi$ ,  $|x\rangle$ ,  $|z\rangle$ )

---

**Input:** Quantum state  $|x\rangle |z\rangle$ , classical value  $\phi$

**Output:** Quantum state  $\exp(i\phi xz) |x\rangle |z\rangle$

1. Split  $|x\rangle$  and  $|z\rangle$  in half, as  $|x_1\rangle |x_0\rangle$  and  $|z_1\rangle |z_0\rangle$
2. Apply PhaseProduct( $(2^n - 2^{n/2})\phi$ ,  $|x_1\rangle$ ,  $|z_1\rangle$ )
3. Apply PhaseProduct( $(1 - 2^{n/2})\phi$ ,  $|x_0\rangle$ ,  $|z_0\rangle$ )
4. Add  $|x_1\rangle$  to  $|x_0\rangle$ , and  $|z_1\rangle$  to  $|z_0\rangle$ . Registers now hold  $|x_1\rangle |x_0 + x_1\rangle |z_1\rangle |z_0 + z_1\rangle$ .
5. Apply PhaseProduct( $2^{n/2}\phi$ ,  $|x_0 + x_1\rangle$ ,  $|z_0 + z_1\rangle$ ).
6. Subtract  $|x_1\rangle$ ,  $|z_1\rangle$  to return to registers to  $|x_1\rangle |x_0\rangle |z_1\rangle |z_0\rangle$ .