

# Fast quantum integer multiplication with very few ancillas

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Gregory D. Kahanamoku-Meyer, Norman Y. Yao

October 19, 2023

Arithmetic on quantum computers: why do we care?

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Existing experimental demonstrations of quantum computational advantage have had the limitation that verifying the correctness of the quantum device requires exponentially costly classical computations. Here we propose and analyse an interactive protocol for demonstrating quantum computational advantage, which is efficiently classically verifiable. Our protocol relies on a class of cryptographic tools called trapdoor claw-free functions. Although this type of function has been applied to quantum advantage protocols before, our protocol employs a surprising connection to Bell's inequality to avoid the need for a demanding cryptographic property called the adaptive hardcore bit, while maintaining essentially no increase in the quantum circuit complexity and no extra assumptions. Leveraging the relaxed cryptographic requirements of the protocol, we present two trapdoor claw-free function constructions, based on Rabin's function and the Diffie-Hellman problem, which have not been used in this context before. We also

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$x^2 \bmod N$  with this result:  
 $\gtrsim 10^6$  gates, 2000 qubits

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### Classically verifiable quantum advantage from a computationally untrusted quantum device

#### A Cryptographic Test of Quantumness and Certifiable Randomness from a Single Quantum Device

Gregory D. Kazar

Existing experimental demonstrations of quantum advantage rely on cryptographic tools called the assumptions. Leveraging

ZVIKA BRAKERSKI, Weizmann Institute of Science, Israel  
PAUL CHRISTIANO, OpenAI, USA  
URMILA MAHADEV, California Institute of Technology, USA  
UMESH VAZIRANI, UC Berkeley, USA  
THOMAS VIDICK, California Institute of Technology, USA

We consider a new model for the testing of untrusted quantum devices, consisting of a single polynomial time bounded quantum device interacting with a classical polynomial time verifier. In this model, we propose solutions to two tasks—a protocol for efficient classical verification that the untrusted device is “truly quantum” and a protocol for producing certifiable randomness from a single untrusted quantum device. Our solution relies on the existence of a new cryptographic primitive for constraining the power

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009

### POLYNOMIAL-TIME ALGORITHMS FOR PRIME FACTORIZATION AND DISCRETE LOGARITHMS ON A QUANTUM COMPUTER\*

PETER W. SHOR<sup>†</sup>

**Abstract.** A digital computer is generally believed to be an efficient universal computing device; that is, it is believed able to simulate any physical computing device with an increase in computation time by at most a polynomial factor. This may not be true when quantum mechanics is taken into consideration. This paper considers factoring integers and finding discrete logarithms, two problems which are generally thought to be hard on a classical computer and which have been used as the basis for

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Existing experimental devices for demonstrating quantum advantage use cryptographic tools called the adversary property. Our solution leverages cryptographic tools called the adversary property. Leveraging cryptographic tools called the adversary property. Leveraging cryptographic tools called the adversary property.

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### POLYNOMIAL-TIME QUANTUM FACTORING

**Abstract.** We show that  $n$ -bit integers can be factored by independently running a quantum circuit with  $\tilde{O}(n^{3/2})$  gates for  $\sqrt{n} + 4$  times, and then using polynomial-time classical post-processing. The correctness of the algorithm relies on a number-theoretic heuristic assumption reminiscent of those used in subexponential classical factorization algorithms. It is currently not clear if the

## An Efficient Quantum Factoring Algorithm

Oded Regev\*

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... with as few gates and qubits as possible.

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$$\mathcal{U}_{q \times q} |x\rangle |y\rangle |w\rangle = |x\rangle |y\rangle |w + xy\rangle$$

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## Background: schoolbook multiplication

Given two  $n$ -bit numbers  $x$  and  $y$ , what if we use base  $b = 2^{n/2}$ ?

$$\begin{array}{r} \phantom{\times} \phantom{y_1} \phantom{y_0} \phantom{x_1} \phantom{x_0} \\ \times \phantom{y_1} \phantom{y_0} \phantom{x_1} \phantom{x_0} \\ \hline \phantom{\times} \phantom{y_1} \phantom{y_0} x_0 y_0 \\ \phantom{\times} \phantom{y_1} x_1 y_0 \\ \phantom{\times} x_0 y_1 \\ + \phantom{x_1} x_1 y_1 \\ \hline \end{array}$$

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Time remains  $\mathcal{O}(n^2)$ , because  $4(n/2)^2 = n^2$

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$$xy = x_1y_1b^2 + (x_0y_1 + x_1y_0)b + x_0y_0$$

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Can compute  $xy$  with only **three** multiplications of size  $\log b = n/2$ :

1.  $x_1y_1$
2.  $x_0y_0$
3.  $(x_1 + x_0)(y_1 + y_0)$

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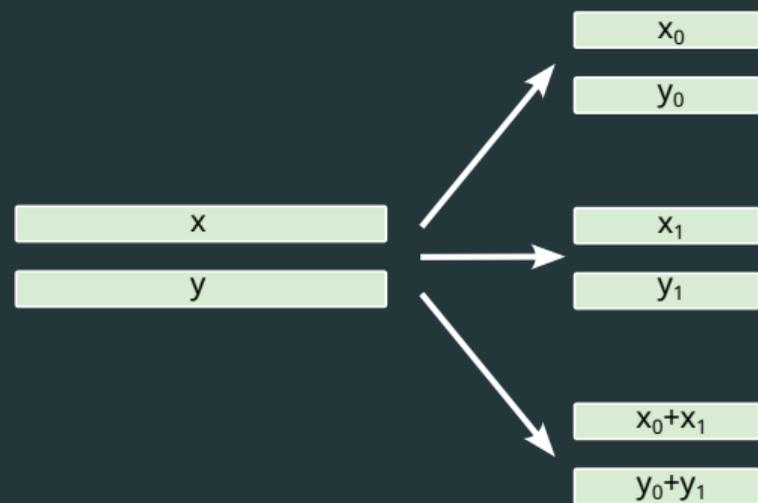
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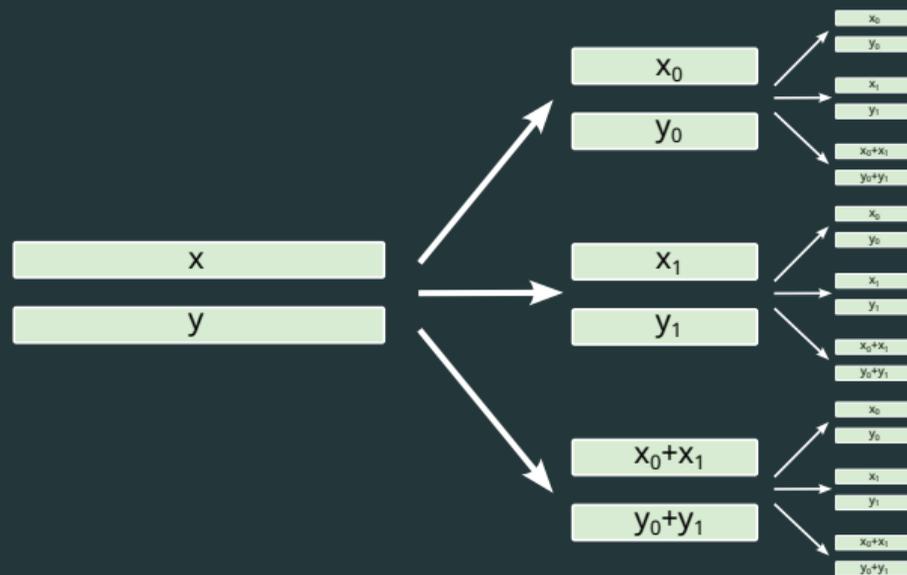
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Computational cost:  $3(n/2)^2 = \frac{3}{4}n^2 = \mathcal{O}(n^2)$

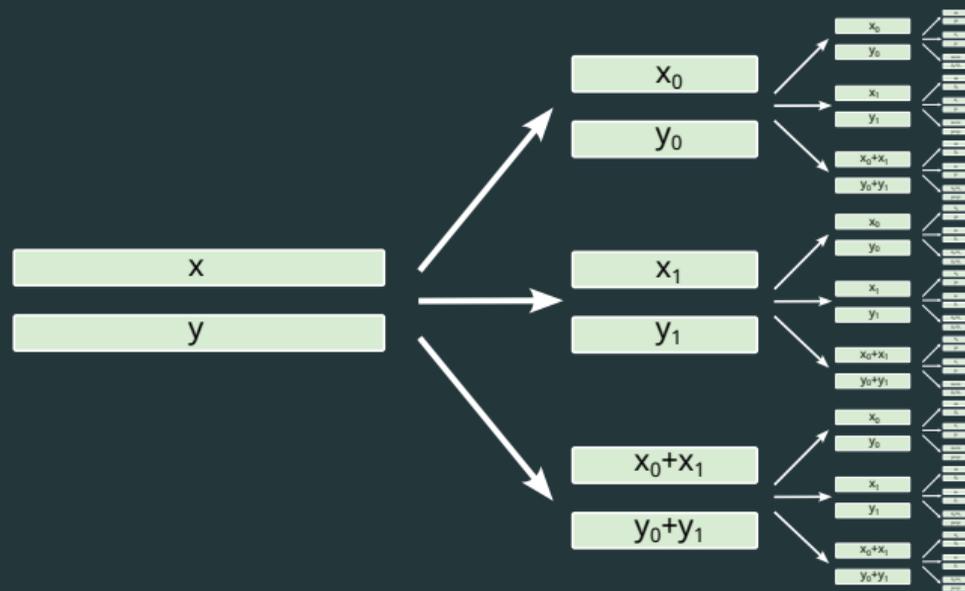
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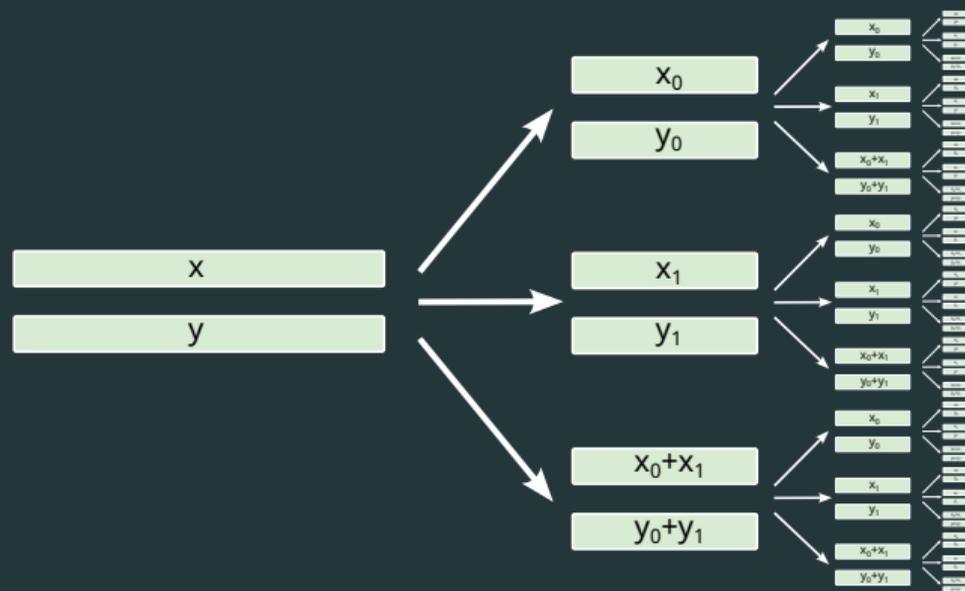
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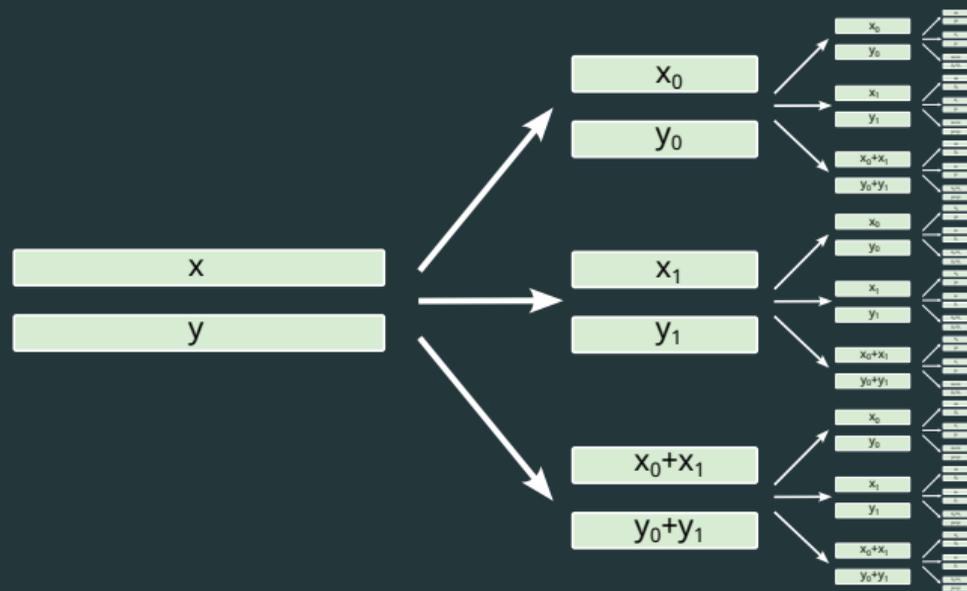


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Depth:  $d = \log_2 n$

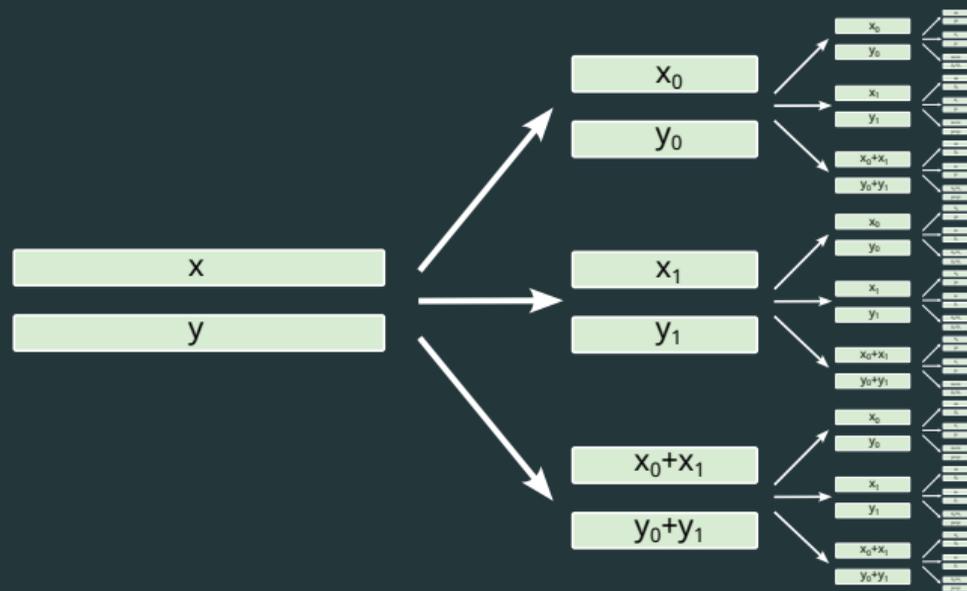
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Depth:  $d = \log_2 n$

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Operations:  $3^d$

Cost:  $\mathcal{O}(n^{\log_2 3}) = \mathcal{O}(n^{1.58\dots})$

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**Question:** why don't we always do this, classically?

**Answer:** the extra complexity isn't always worth it!

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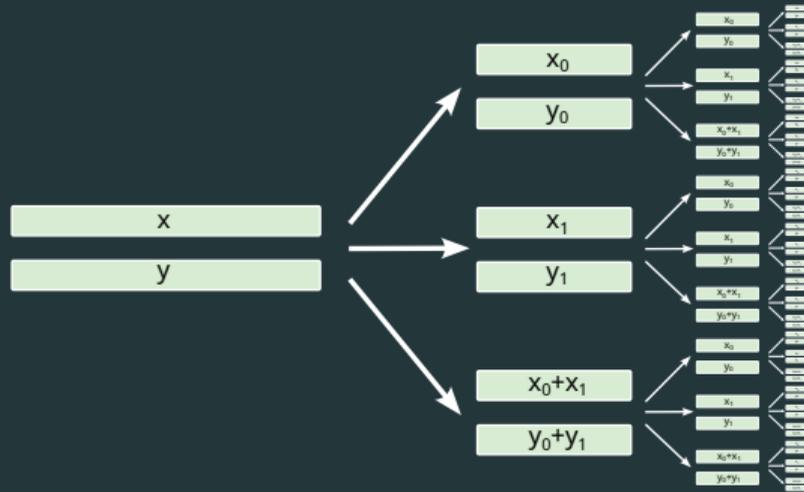
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GNU multiple-precision arithmetic library cutoff: 2176 bit numbers

Can these fast circuits be made quantum?

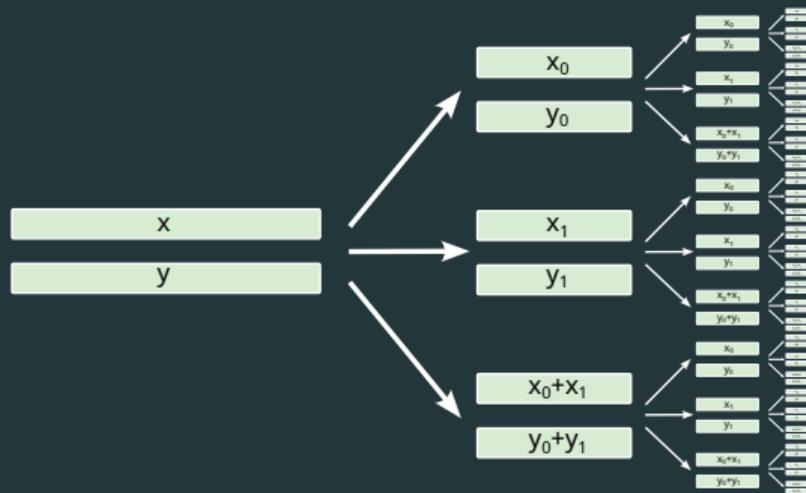
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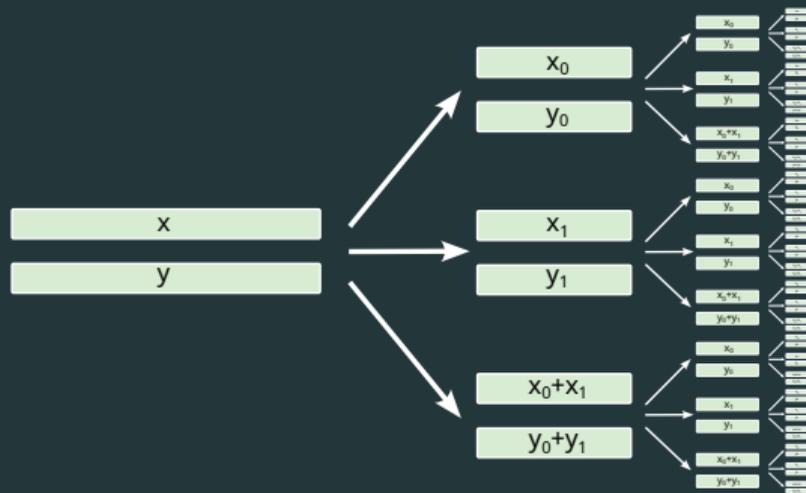
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| Work              | Qubits                       |
|-------------------|------------------------------|
| Kowada et al. '06 | $\mathcal{O}(n^{1.58\dots})$ |
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| Gidney '19        | $\mathcal{O}(n)$             |

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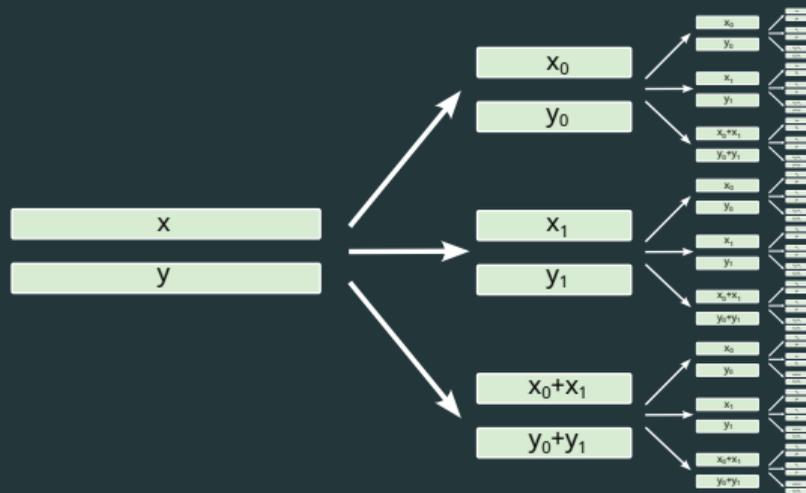


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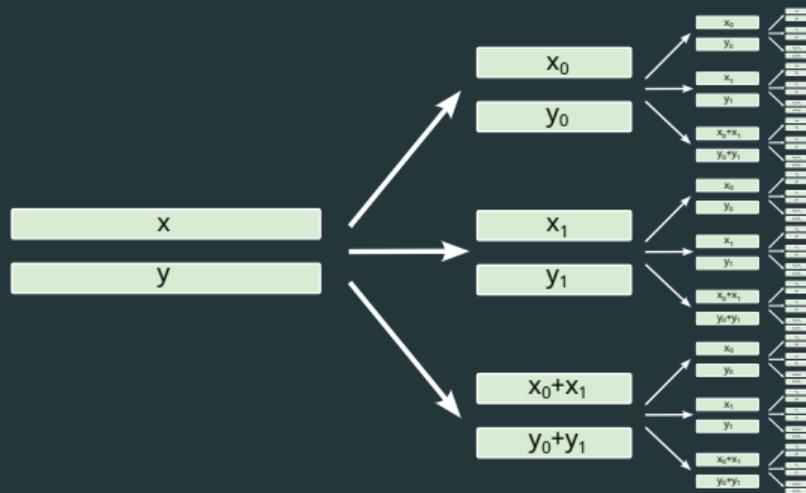
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**Result:** Fast multiplication using 1 ancilla

# A fundamentally quantum way of doing arithmetic

[Draper '04]: Arithmetic in Fourier space

$$|xy\rangle = \text{QFT}^{-1} \sum_z \exp\left(\frac{2\pi ixyz}{2^n}\right) |z\rangle$$

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$x_i, y_j, z_k$  are binary values—apply phase only if they all are equal to 1!

A series of  $CCR_\phi$  gates between the bits of  $|x\rangle$ ,  $|y\rangle$ , and  $|z\rangle$ !

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$$\exp\left(\frac{2\pi iaxz}{2^n}\right) = \prod_{i,j} \exp\left(\frac{2\pi ia2^{i+j}}{2^n} x_i z_j\right)$$

Here:  $\mathcal{O}(n^2)$  controlled phase rotations (matches Schoolbook algorithm)

**Main question:** Can we combine fast multiplication with Fourier arithmetic to get the benefits of both?

# Fast classical-quantum multiplication

Goal:  $\mathcal{U}(a) |x\rangle |0\rangle = |x\rangle |ax\rangle$

# Fast classical-quantum multiplication

Goal: Apply phase  $\exp\left(\frac{2\pi ia}{2^n}xz\right)$ ;  $x$  and  $z$  are quantum

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Goal: Implement  $\text{PhaseProduct}(\phi) |x\rangle |z\rangle = \exp(i\phi xz) |x\rangle |z\rangle$

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**Goal:** Implement  $\text{PhaseProduct}(\phi) |x\rangle |z\rangle = \exp(i\phi xz) |x\rangle |z\rangle$

We want to split the phase  $\phi xz$  into the sum of many phases, which are easy to implement.

# Fast classical-quantum multiplication

Goal: Implement  $\text{PhaseProduct}(\phi) |x\rangle |z\rangle = \exp(i\phi xz) |x\rangle |z\rangle$

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Previously:

$$\exp(i\phi xz) = \prod_{i,j} \exp\left(i\phi 2^{i+j} x_i z_j\right)$$

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**Karatsuba:**

$$xz = 2^n x_1 z_1 + 2^{n/2} ((x_0 + x_1)(z_0 + z_1) - x_0 z_0 - x_1 z_1) + x_0 z_0$$

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**Plugging in Karatsuba:**

$$\begin{aligned} \exp(i\phi xz) &= \exp(i\phi 2^n x_1 z_1) \\ &\quad \cdot \exp(i\phi x_0 z_0) \\ &\quad \cdot \exp\left(i\phi 2^{n/2} ((x_0 + x_1)(z_0 + z_1) - x_0 z_0 - x_1 z_1)\right) \end{aligned}$$

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How are we supposed to **reuse** values in the *phase*?

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**Re-ordering Karatsuba:**

$$xz = (2^n - 2^{n/2})x_1z_1 + 2^{n/2}(x_0 + x_1)(z_0 + z_1) + (1 - 2^{n/2})x_0z_0$$

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We want to split the phase  $\phi xz$  into the sum of many phases, which are easy to implement.

Plugging in reordered Karatsuba:

$$\begin{aligned} \exp(i\phi xz) &= \exp\left(i\phi(2^n - 2^{n/2})x_1z_1\right) \\ &\quad \cdot \exp\left(i\phi(1 - 2^{n/2})x_0z_0\right) \\ &\quad \cdot \exp\left(i\phi 2^{n/2}(x_0 + x_1)(z_0 + z_1)\right) \end{aligned}$$

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Each of these has the same structure, but on half as many qubits  $\rightarrow$  do it recursively!

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Recursion relation:  $T(n) = 3T(n/2) \Rightarrow \mathcal{O}(n^{\log_2 3}) = \mathcal{O}(n^{1.58\dots})$  gates!

## How many qubits do we need?

Splitting registers  $|x\rangle \rightarrow |x_1\rangle |x_0\rangle$  and  $|z\rangle \rightarrow |z_1\rangle |z_0\rangle$ , can immediately do

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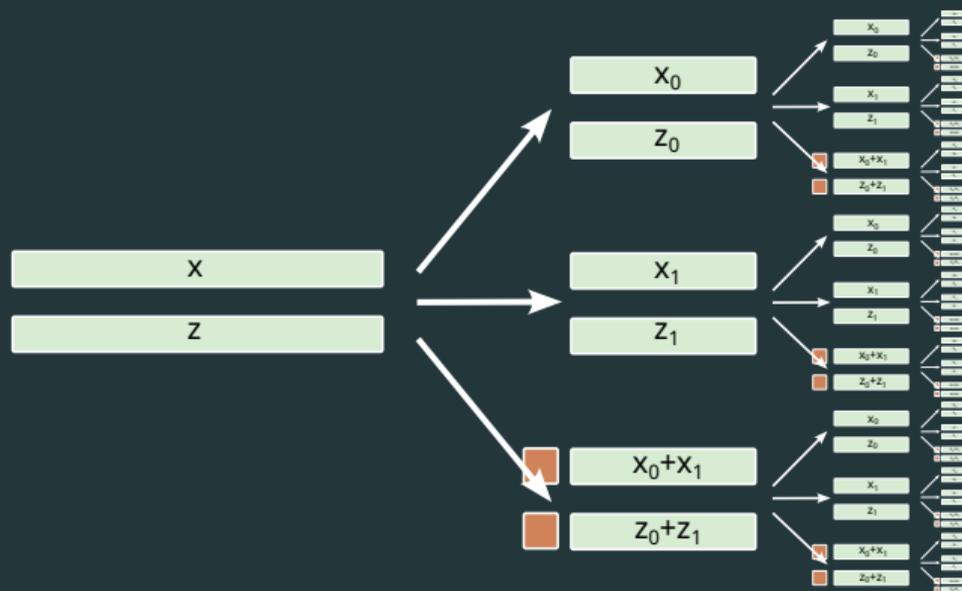
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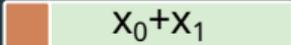
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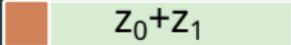


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Total number of ancillas:  $\mathcal{O}(\log n)$

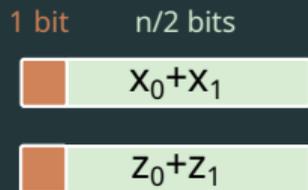
1 bit      n/2 bits

  $X_0 + X_1$

  $Z_0 + Z_1$

## How many qubits do we need?

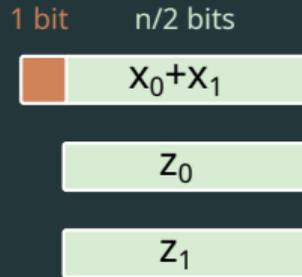
Total number of ancillas: 2



Idea: “Shave off” the high bit before recursing

# How many qubits do we need?

Total number of ancillas: 1



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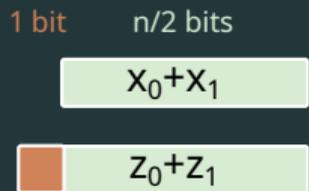
$Z_0$

$Z_1$

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## Making it go faster

So far:  $\mathcal{O}(n^{1.58})$  gates using 1 ancilla

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Can we make it go faster?

## Background: Toom-Cook multiplication

Let  $b = 2^{n/2}$ .

$$x = x_1b + x_0$$

$$z = z_1b + z_0$$

n/2 bits

$x_0$

n/2 bits

$x_1$

$z_0$

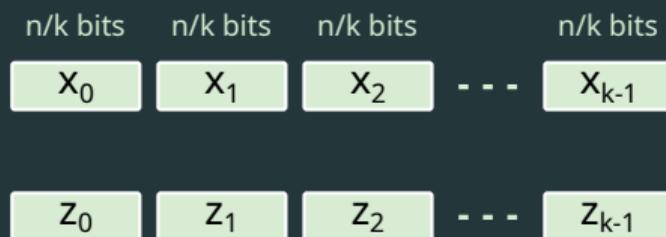
$z_1$

## Background: Toom-Cook multiplication

Let  $b = 2^{n/k}$ .

$$x = \sum_{i=0}^{k-1} x_i b^i$$

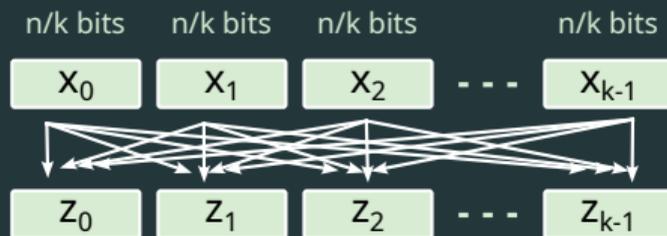
$$z = \sum_{i=0}^{k-1} z_i b^i$$



## Background: Toom-Cook multiplication

Let  $b = 2^{n/k}$ .

$$xZ = \left( \sum_{i=0}^{k-1} x_i b^i \right) \left( \sum_{i=0}^{k-1} z_i b^i \right)$$



Schoolbook:  $k^2$  multiplications of size  $n/k$

## Background: Toom-Cook multiplication

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Facts:

- For any point  $w$ ,  $p(w) = x(w)z(w)$

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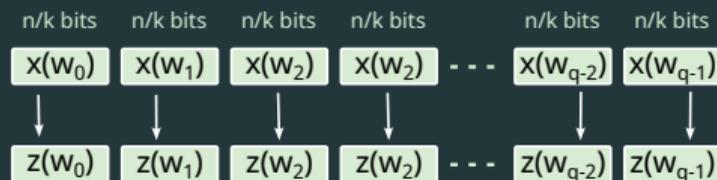
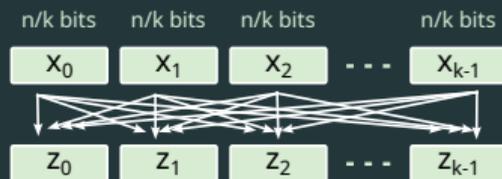
$$p(2^{n/k}) = x(2^{n/k})z(2^{n/k})$$

### Facts:

- For any point  $w$ ,  $p(w) = x(w)z(w)$
- $p(b)$  has degree  $2(k-1) \Rightarrow$  uniquely determined by  $q = 2(k-1) + 1$  points  $w_\ell$ !

# Background: Toom-Cook multiplication

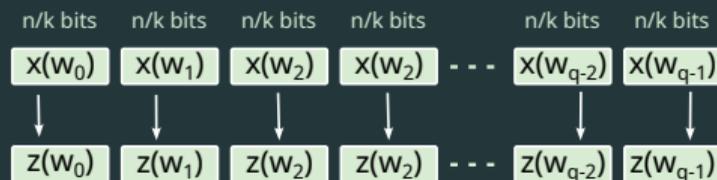
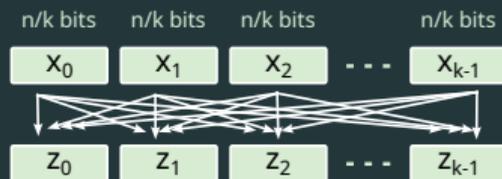
1. Compute  $x(w_\ell), z(w_\ell)$  at  $q$  points  $w_\ell$



Only  $2k - 1$  multiplications of size  $n/k$ !

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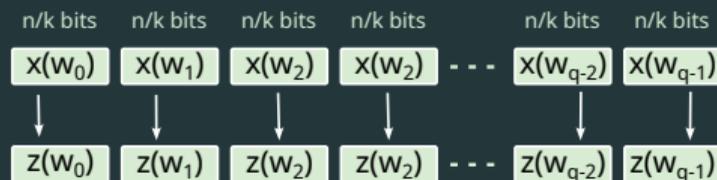
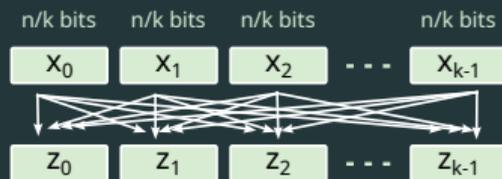
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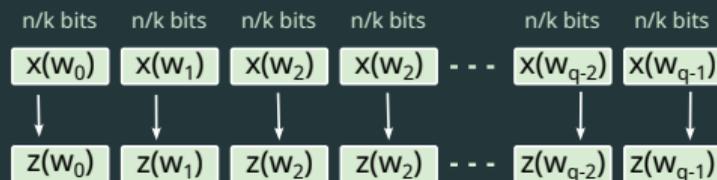
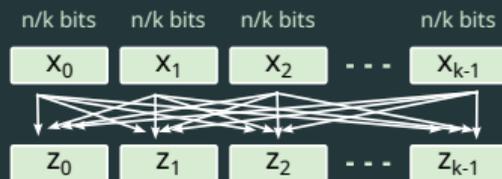
1. Compute  $x(w_\ell), z(w_\ell)$  at  $q$  points  $w_\ell$
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## Complexity vs. $k$

Toom-Cook has asymptotic complexity  $\mathcal{O}(n^{\log_k(2k-1)})$

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| Algorithm  | Gate count                   |
|------------|------------------------------|
| Schoolbook | $\mathcal{O}(n^2)$           |
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| $k = 3$    | $\mathcal{O}(n^{1.46\dots})$ |
| $k = 4$    | $\mathcal{O}(n^{1.40\dots})$ |
| $\vdots$   | $\vdots$                     |

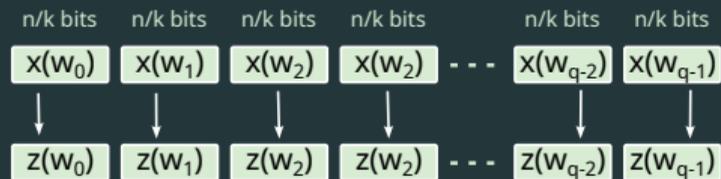
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These are the gate counts for our classical-quantum multiplication!

# Overhead moves to classical precomputation

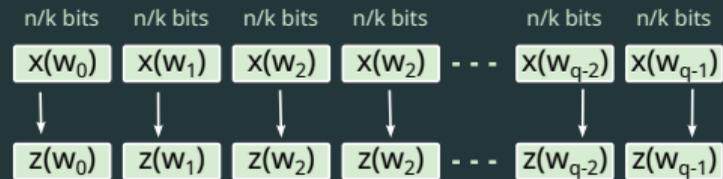
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$$\phi_{XZ} = \sum_{\ell=0}^{2k-2} \phi_\ell x(w_\ell) z(w_\ell) \quad (1)$$

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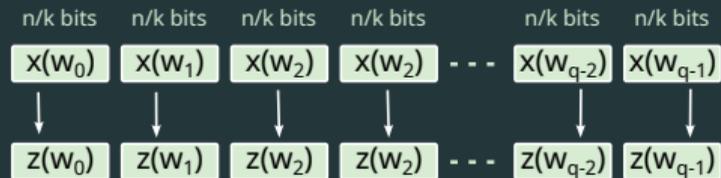
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$$\phi_{XZ} = \sum_{\ell=0}^{2k-2} \phi_\ell x(w_\ell) z(w_\ell) \quad (1)$$

Much of the overhead has moved to classical precomputation!

## Fast quantum-quantum multiplication

Goal:  $\mathcal{U} |x\rangle |y\rangle |0\rangle = |x\rangle |y\rangle |xy\rangle$

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Goal: Apply phase  $\exp\left(\frac{2\pi i}{2^n}xyz\right)$ ;  $x$ ,  $y$ , and  $z$  are quantum

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Doesn't work in the phase!!

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$$p(b) = x(b)y(b)z(b)$$

$p(b)$  has degree  $q = 3k - 2$

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For  $k = 2$ , we have  $q = 4$ . Using  $w_i \in \{0, \infty, 1, -1\}$ :

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| $k$      | Gates $\mathcal{O}(n^{\log_k(3^k-2)})$ |
|----------|--|
| 1*       | $\mathcal{O}(n^3)$                     |
| 2        | $\mathcal{O}(n^2)$                     |
| 3        | $\mathcal{O}(n^{1.77\dots})$           |
| 4        | $\mathcal{O}(n^{1.66\dots})$           |
| 5        | $\mathcal{O}(n^{1.59\dots})$           |
| 6        | $\mathcal{O}(n^{1.55\dots})$           |
| $\vdots$ | $\vdots$                               |

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## Fast exact quantum Fourier transform

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For any  $m < n$ , we may implement  $\text{QFT}_{2^n}$ :

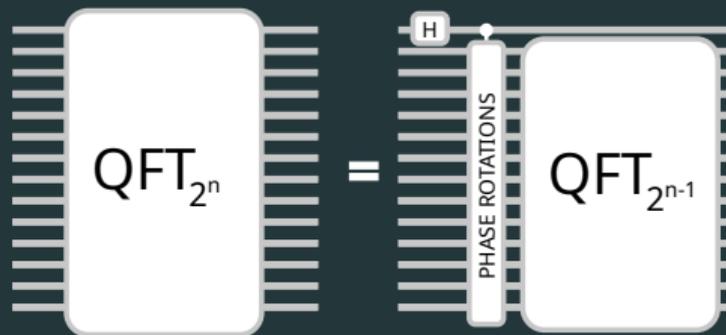
1. Apply  $\text{QFT}_{2^m}$  on first  $m$  qubits
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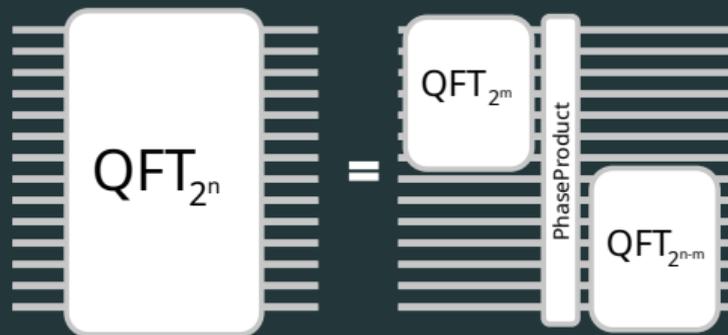


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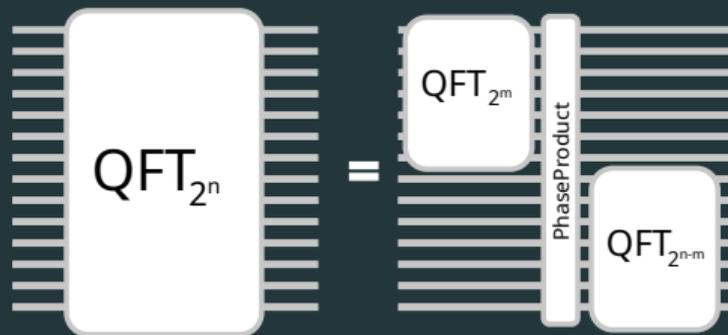


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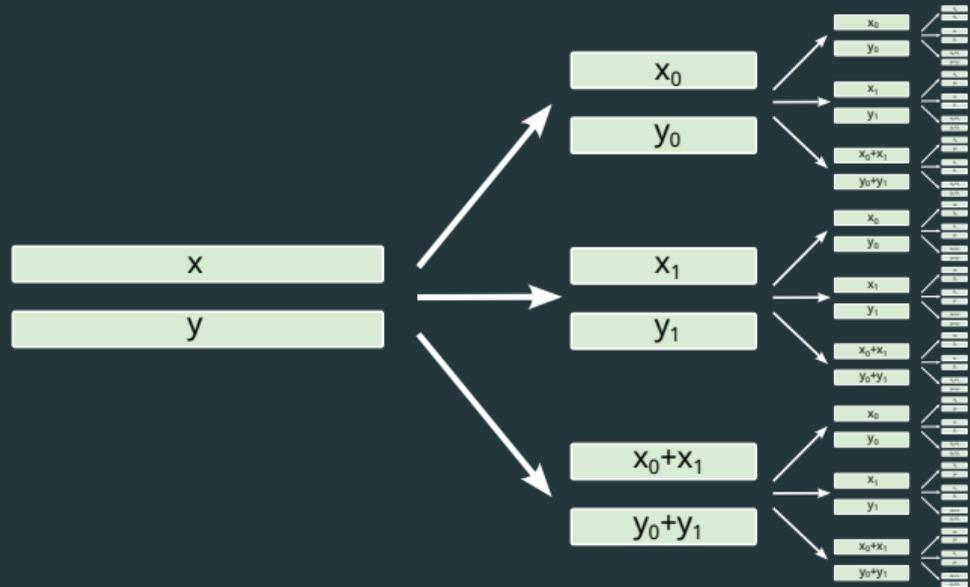
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Immediately gives us sub-quadratic exact QFT using only 1 ancilla.

# Depth considerations

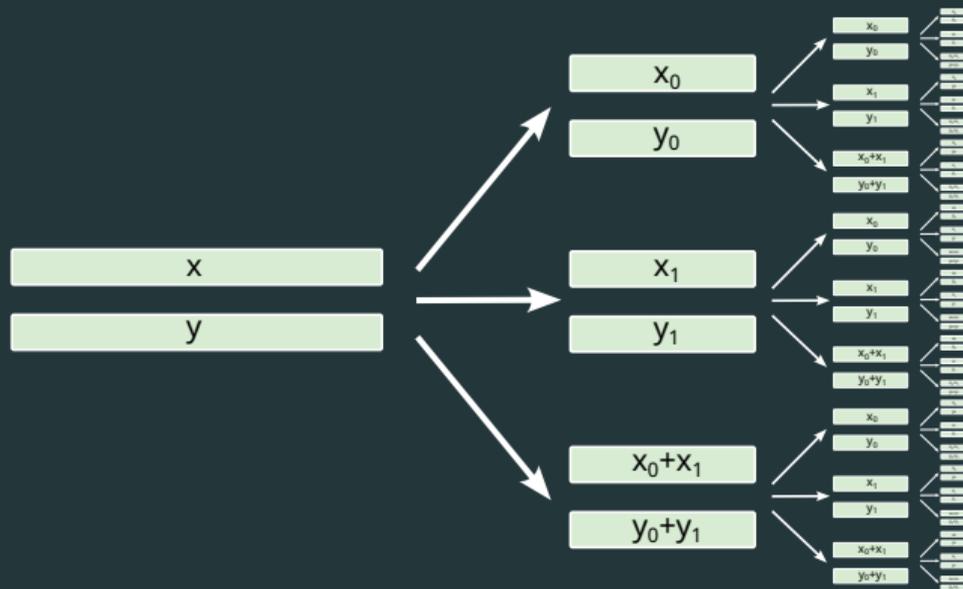
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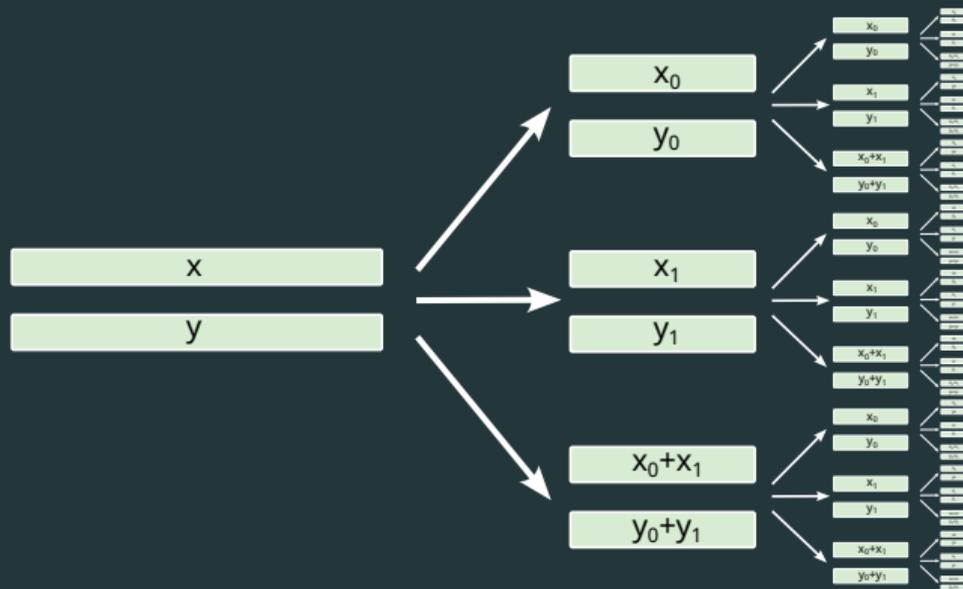
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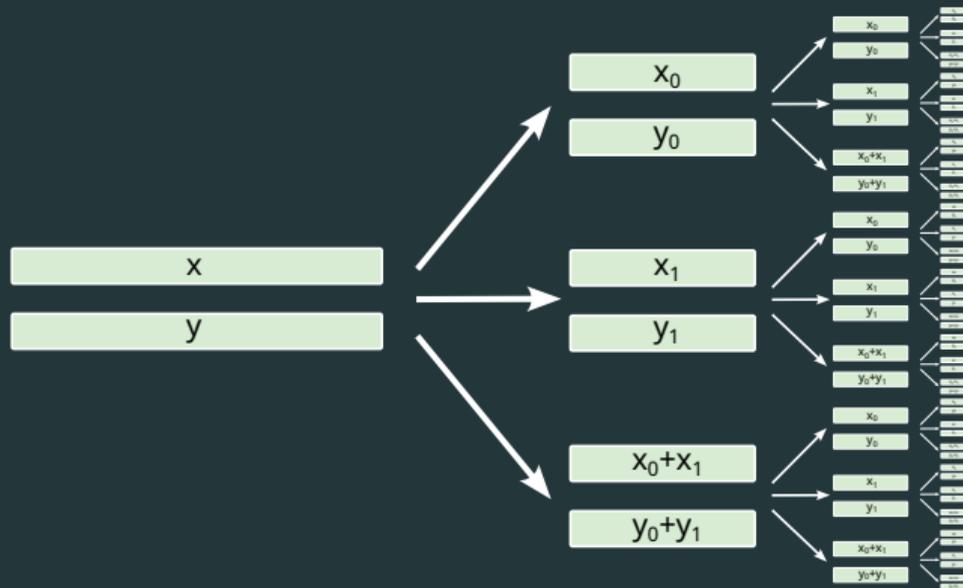


**Depth:** PhaseProduct in  $\mathcal{O}(n^{\log_k 2})$  and PhaseTripleProduct in  $\mathcal{O}(n^{\log_k 3})$   
using a few more ancillas

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**Challenge for multiply:** How to do the QFT in sublinear depth with even  $\mathcal{O}(n)$  ancillas?

So far: have been using phase

$$\exp\left(2\pi i \frac{xyz}{2^n}\right)$$

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**Observation:**

$$\exp\left(2\pi i \frac{xyz}{N}\right) = \exp\left(2\pi i \frac{(xy \bmod N)z}{N}\right)$$

# Modular arithmetic

**Goal:** only use  $n$  bits for output modulo  $N$

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Output register requires  $n + \mathcal{O}(\log(1/\epsilon))$  qubits

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Using phase modulo and  $k = 4$  multiplier:

Gates:  $\mathcal{O}(n^{2.4})$

Total qubits:  $2n + \mathcal{O}(\log(n/\epsilon))$

(Here  $\epsilon$  is error across the whole algorithm)

## Application: Shor's algorithm

Cost estimates for one 2048-bit classical-quantum multiplication: (here not modular)

| Algorithm        | Complexity              | Gate count (millions) |            |            | Ancilla qubits |
|------------------|-------------------------|-----------------------|------------|------------|----------------|
|                  |                         | Toffoli               | $CR_\phi$  | Other      |                |
| <b>This work</b> | $\mathcal{O}(n^{1.4})$  | <b>0.6</b>            | <b>0.9</b> | <b>2.1</b> | <b>50</b>      |
| Karatsuba [1]    | $\mathcal{O}(n^{1.58})$ | 5.6                   | —          | 34         | 12730          |
| Windowed [1]     | $\mathcal{O}(n^2)$      | 1.8                   | —          | 2.5        | 4106           |
| Schoolbook [1]   | $\mathcal{O}(n^2)$      | 6.4                   | —          | 38         | 2048*          |

(Note:  $\sim 15\%$  of the  $CR_\phi$  come from approximate QFTs with  $\epsilon = 10^{-12}$ )

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**Open q.:** Can we use windowing with our construction?

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Cost estimates for protocol with 1024-bit  $N$ :

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|                          | Toffoli               | $C^*R_\phi$ | Other |              |
| Gate optimized           | 0.7                   | 0.9         | 0.7   | 2400         |
| Balanced                 | 0.9                   | 1.0         | 0.9   | 2070         |
| Qubit optimized          | 2.2                   | 2.0         | 2.2   | 1560         |
| “Digital” Karatsuba [2]  | 1.6                   | —           | 1.6   | 6801         |
| “Digital” Schoolbook [2] | 3.5                   | —           | 2.9   | 4097         |
| Prev. Fourier 1 [2]      | —                     | 539         | —     | 1025         |
| Prev. Fourier 2 [2]      | —                     | 35          | —     | 2062         |

[2] GDKM, Choi, Vazirani, Yao. “Efficiently-verifiable quantum advantage from a computational Bell test.” (arXiv:2104.00687)

# Summary

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**Low crossover**—in some cases, already faster for 20 bit inputs!

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- How well can we optimize explicit circuits (especially the base case)?

**Thank you!**

Greg Kahanamoku-Meyer — [gkm@berkeley.edu](mailto:gkm@berkeley.edu)

# Backup

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### Direct (schoolbook)

Apply  $32^2 = 1024$   $CR_\phi$  gates

### $CR_\phi$ optimized

1. Compute  $|x'z'\rangle$  via a regular digital multiplier circuit
2. Apply phase rotations on the output
3. Uncompute  $|x'z'\rangle$

## What about all the arbitrary rotation gates?

In error-corrected setting, arbitrary rotation gates need to be synthesized.

**Idea:** “convert” some rotation gates into e.g. Toffolis; easier to synthesize

All rotations are in the **base case**: 32-bit (say) PhaseProduct  $\phi x'z'$

### Direct (schoolbook)

Apply  $32^2 = 1024$   $CR_\phi$  gates

### $CR_\phi$ optimized

1. Compute  $|x'z'\rangle$  via a regular digital multiplier circuit
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$1024 CR_\phi \rightarrow 64 R_\phi$  plus  $\sim 2048$  Toffoli

# Fast classical-quantum multiplication: algorithm

PhaseProduct( $\phi, |x\rangle, |z\rangle$ )

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**Input:** Quantum state  $|x\rangle |z\rangle$ , classical value  $\phi$

**Output:** Quantum state  $\exp(i\phi xz) |x\rangle |z\rangle$

1. Split  $|x\rangle$  and  $|z\rangle$  in half, as  $|x_1\rangle |x_0\rangle$  and  $|z_1\rangle |z_0\rangle$
2. Apply PhaseProduct( $(2^n - 2^{n/2})\phi, |x_1\rangle, |z_1\rangle$ )
3. Apply PhaseProduct( $(1 - 2^{n/2})\phi, |x_0\rangle, |z_0\rangle$ )
4. Add  $|x_1\rangle$  to  $|x_0\rangle$ , and  $|z_1\rangle$  to  $|z_0\rangle$ . Registers now hold  $|x_1\rangle |x_0 + x_1\rangle |z_1\rangle |z_0 + z_1\rangle$ .
5. Apply PhaseProduct( $2^{n/2}\phi, |x_0 + x_1\rangle, |z_0 + z_1\rangle$ ).
6. Subtract  $|x_1\rangle, |z_1\rangle$  to return to registers to  $|x_1\rangle |x_0\rangle |z_1\rangle |z_0\rangle$ .